



PATENT

2023796-7036172001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Jon C. Taenzer

Serial No.: 09/610,188

Filed: July 5, 2000

For: IMPROVED DIRECTIONAL
MICROPHONE SYSTEM

Confirmation No.: 5001

Group Art Unit: 2644

Examiner: Pendleton, Brian T.

RESPONSE TRANSMITTALMail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is a Response (6 pages) to Office Action, mailed June 23, 2005, for the above-identified application.

The Commissioner is hereby authorized to charge any fees which may be required to Deposit Account No. 50-2518, Docket No. 2023796-7036172001.

DATE: September 19, 2005

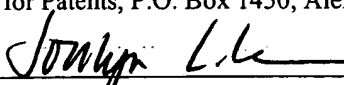
Respectfully submitted,
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I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

9/20/05
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RESPONSE

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Dear Sir:

This letter is responsive to the Office Action mailed June 23, 2005. Claims 1-35 remain pending in this application, and have not been amended. Based on the following remarks, reconsideration and allowance of this application is respectfully requested.

Claim Rejections-35 U.S.C. §102

Claims 1 and 4

Claims 1 and 4 stand rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 3,715,500 issued to Sessler et al. ("Sessler"). Applicant respectfully traverses this rejection, since Sessler does not disclose each and every element of these claims.

In particular, independent claim 1 requires each of the first-order microphone elements to have a finite delay ratio (DR) greater than one. As is well known in the art, and as described in the background of the application, the DR is defined as the internal delay τ_d of one of the ports of the first-order microphone divided by the propagation delay τ_p between the two ports. See page